

SHEET INDEX

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CIRCUIT NOTES:

101.	DESIS	FUSE AMP	POTENTIAL	ONE PER
	1-1/2		-40V TALK	CKT
BATTERY SYMBOL		VOLTAGE RANGE		

102.

FEATURE OR OPTION	PROVIDE		REMOVE
	APP FIG.	QUANTITY	APP OR URG
KS-19328, L1 AMPLIFIER	1	ONE PER CKT	
HIGH GAIN		W	
LOW GAIN		Z	
LOW GAIN - HIGH DC RES INPUT		V	
OUTPUT IMPEDANCE	150 OHMS	Y	
	600 OHMS	X	
BATTERY GROUND	POSITIVE GROUND	U	
	NEGATIVE GROUND	T	

103.

NETWORK VALUES		
NETWORK NO.	RESISTANCE IN OHMS	CAPACITANCE IN UF

104.

RECORD OF APP FIGURES, WIRING AND APPARATUS CHANGES						
CHANGED ON 155	IF 208 RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	USE IN CIRCUIT	STD	A & H	NO

SUPPORTING INFORMATION

CATEGORY	NO.

OPTION INDEX

APP FIG. NO.	LOCATION
Z	2C2, 200
Y	2C5
X	2C5
W	2C1, 200
V	2C1, 2C2
U	205
T	205

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS,
CAPACITANCE VALUES ARE IN MICROFARADS.

NOTICE
THIS FIGURE IS THE
PROPERTY OF BELL
TELEPHONE
LABORATORIES

ISSUE
4D

SD-99316-01

2ND

COMMON SYSTEMS

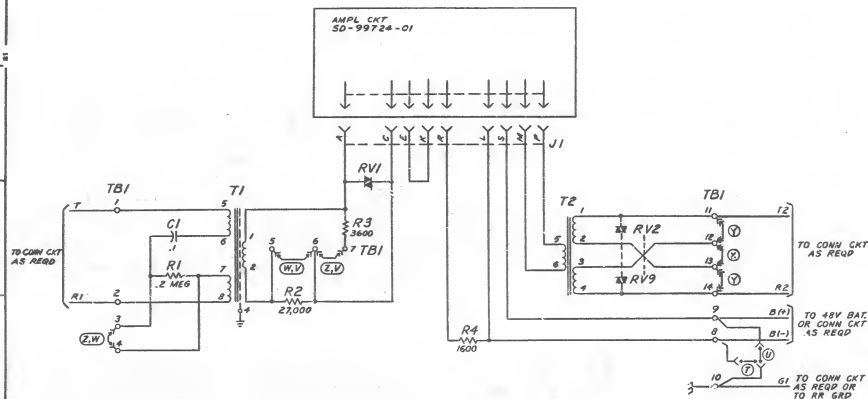
KS-19328, L1 AMPLIFIER CIRCUIT

R&P ONLY

BELL TELEPHONE LABORATORIES
INCORPORATED

6S

SD-99316-01-1
2 SHEETS



APP FIG. 1

CAPACITOR

DESIG	LOC	CODE
C1	2C1	55548

RESISTOR

DESIG	LOC	CODE
R1	2C1	KS-19150, L1, .2 MEG
R2	2C1	KS-19150, L1, 27,000
R3	2C2	KS-19150, L1, 3600
R4	2C3	KS-19150, L1, 1600

TRANSFORMER

DESIG	LOC	CODE
T1	2C1	2543J
T2	2C3	2543K

CONNECTOR
KS-16345, L2

DESIG	J1
OPTION	
A	2B3
B	2B3
C	2B3
D	2B3
E	2B3
F	2B3
G	2B3
H	2B3
I	2B3
J	2B3
K	2B3
L	2B3
M	2B3
N	2B3
O	2B3
P	2B3
Q	2B3
R	2B3
S	2B3

TERMINAL BOARD

DESIG	TB1
OPTION	
14	2C4
13	2C4
12	2C4
11	2C4
10	2C4
9	2C4
8	2C4
7	2C4
6	2C4
5	2C4
4	2C4
3	2C4
2	2C4
1	2C4

VARIATOR

DESIG	LOC	CODE
RV1	2B2	100A
RV2-9	2C4	100A